Dot Matrix LED Units LT1460ED

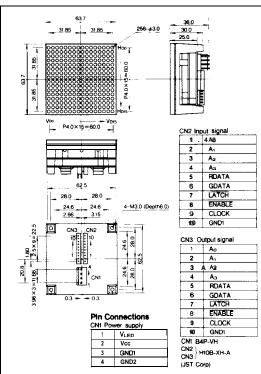
LT1460ED

■ Features

- 1. 16X 16 dot matrix LED unit
- 2. Active display size: 63.7mm square
- 3. Three color emission by use of dichromatic LED
- 4. Radiation color: Red, yellow-green and orange (mixed color)
- 5. Wide viewing angle
- 6. Built-in shift registers, latch circuits, LED driver ICS and scanning line select circuits
- 7. Clock frequency: 3MHz
- 8. Dynamic drive (Duty ratio: 1/16)

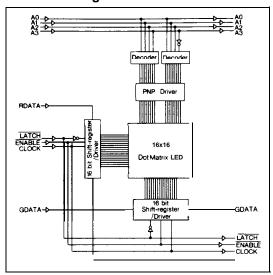
■ Outline Dimensions

(Unit: mm)



16X 16 Dot Matrix LED Unit for Outdoor Use

■ Block Diagram



■ Terminal Functions

Connector	in No.	Name	Function		
CN1 (Power supply)	1	VLED	Power supply for LED		
	2	V_{cc}	Power supply for IC		
	3	GND1	Ground for IC		
	4	GND2	Grmmd for LED		
CN2	1-4	A ₀ ~A ₃ Address specification signal for row driver			
	5	RDATA	Serial data input for red (H: lit, L: no lit)		
	6	GDATA Serial data input for Yellow-green (H: lit, L: no lit)			
/ Input \	7	LATICH	IL: The contents are latched		
(signal)	8	ENABLE	"L": Each dot can be driven in accordance with data		
	9	Cloock ssignal for data transmi CLOCK sion in the shift-register. (IL-H The data are shifted)			
	10	GND1	Ground for IC		
CN3 (Output (signal)	1~4	$A_0 \sim A_3$	Buffered the input signals A ₀ ~A ₃		
	5	RDATA Input signal is generated through			
	6	CDATA 6-bit shift register in the unit.			
	7	LATCH	Buffered the input signal LATCH.		
	8	ENABLE	Buffered the input signal ENABLE		
	9	CLOCK	Buffered the input signal CLOCK.		
	10	GND1	Ground for IC		

-SHARP

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Parameter	Symbol	Rating	Unit
IC supply voltage	Vcc	6.0	V
LED supply voltage	VLED	6.0	V
Input voltage	V_1	*15.5	V
LED current dissipation	I_{LED}	*22.4	A
Operating temperature range	Topr	−lo to +45	°C
Storage temperature range	Tstg	-20 to +70	°C

■ Electro-optical Characteristics

 $(Ta = 25^{\circ}C, V_{cc} = 5V, V_{LED} = 5V)$

Parameter		Symbol	MIN	TYP	MAX	Unit
Operating IC supply voltage		V_{cc}	4.75	5.0	5.25	v
Oprating LED supply voltage		$V_{\scriptscriptstyle LED}$		5.0	5.25	v
IC current dissipation		I_{cc}		50		mA
LED current dissipation		I_{LED}		*32.1		A
Input voltage		$ m V_{IL}$		_	1.5	V
		V_{1H}	3.5			v
Input current		IIL		_	0.12	mA
		I _{IH}			0.1	μA
Clock frequency		$f_{\scriptscriptstyle \mathrm{CLK}}$			3.0	MHz
Frame frequency		f _{FR}	70	100		Hz
*'Luminance	Red		120	150	190	cd/m²
	Yellow-green	$L_{\rm v}$	120	150	190	
Peak emission w avelangth	Red			635		nm
	Yellow-green	λp		565		
Spectrum radia- tion bandwidth	Red			35		nm
	Yellow-green	Δλ		30		

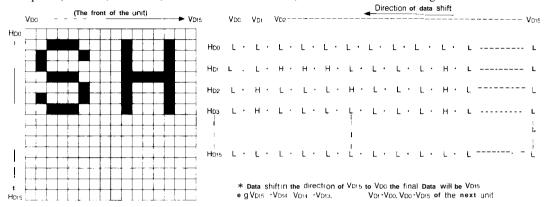
^{*3} Duty ratio: 1/6. When all dots are lit, $f_{ER} = 100 Hz$

^{*1} $V_1{\le}V_{cc}$ at $V_{cc}{\le}5.5$ *2 When all dots are lit, Duty ratio: 1/16

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■ Interface Signals

1, Data being displayed corresponds to a dot pattern, where each dot is either on or off (high= on, low= off). The data for each dot is clocked into a register in a serial, synchronous fashion by the data transmission clock (CLOCK). The data scan direction is from right to left, thus the first bit input will control the left most dot in a row (scan line). Data is input for one row at a time. There are two data inputs (RDATA, GDATA). One is for the red LED, and the other is for the green LED.

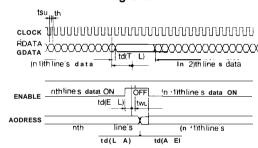


2. When all of the data for one row has been clocked into the shift-register, a data latch signal (LATCH) should be enabled. The data latch signal sends the data from the shift-register to a line driver register. The data remains in the line driver register until another data latch signal occurs. Once the data is in the line driver register, the row for which it was intended, is enabled by the enable clock (ENABLE). The appropriate dots are then turned on or off. While one row is being enabled, the data for the next row is being clocked in.

The address $(A_0 \cdot A_3)$ for the desired row must be present at the same time or after data latch signal, and prior to the enable signal,

 LT1460ED is driven dynamically, so you must scan each row continuously to maintain the correct display.

Timing chart



■ Connections Method

